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Williams, A.C.; Brown, A.D.; Zwolinski, M.;  
Computers and Digital Techniques, IEE Proceedings-  
Volume 147, Issue 6, Nov. 2000 Page(s):383 - 390  
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- ☒ 2. **Using Spice and behavioral synthesis tools to optimize ASIC consumption**  
Martin, R.S.; Knight, J.P.;  
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Volume 2, 13-16 Aug. 1995 Page(s):1209 - 1212 vol.2  
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- ☐ 3. **A low-power Viterbi decoder design for wireless communication applications**  
Ranpara, S.; Dong Sam Ha;  
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15-18 Sept. 1999 Page(s):377 - 381  
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- ☐ 4. **IEEE International Symposium on Circuits and Systems (ISCAS Cat. No. 05CH37618)**  
Circuits and Systems, 2005. ISCAS 2005. IEEE International Syn  
23-26 May 2005  
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- ☐ 5. **Induction motor drive parameters identification**  
Attaianese, C.; Damiano, A.; Gatto, G.; Marongiu, I.; Perfetto, A.;  
Power Electronics, IEEE Transactions on  
Volume 13, Issue 6, Nov. 1998 Page(s):1112 - 1122